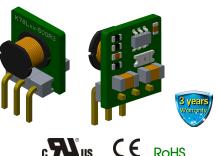


Wide input voltage , non-isolated & regulated single output



FEATURES

- High efficiency up to 95%
- No-load input current as low as 0.2mA
- Operating temperature range: -40°C to +85°C
- Support the negative output
- Output short circuit protection
- Pin-out compatible with LM78XX linear regulators
- UL60950, EN60950 approval

K78Lxx-500R3 series are high efficiency switching regulators and ideal substitutes of LM78xx series three-terminal linear regulators. The product is featured with high efficiency, low loss, short circuit protection, support the negative output and no heat sink requirement. They are widely used in industrial control, instrumentation, and electric power applications.

Selection Guide								
	Part	Input Voltage (VDC) Output		Efficiency (%/Typ.)	Max.			
Certification	Number	Nominal (Range)	Output Voltage (VDC)	Max. Output Current (mA)	(Min. Vin)/ (Max. Vin) @Full Load	Capacitive Load(µF)		
	K78L03-500R3	24 (4.75-36)	3.3	500	86/80	680		
	K78L05-500R3	24 (6.5-36)	5.0	500	90/84	680		
		12 (7-31)	-5.0	-300	80/81	330		
UL/CE	K78L12-500R3	24 (15-36)	12	500	94/91	680		
		12 (8-24)	-12	-150	84/85	330		
	K78L15-500R3	24 (19-36)	15	500	95/93	680		
		12 (8-21)	-15	-150	85/87	330		

Note:For input voltage higher than 30 VDC, a 22uF/50V input capacitor is required.

Input Specifications							
Item	Operating Conditions	Min.	Тур.	Max.	Unit		
No-load Input Current	Positive output		0.2	1.5	mA		
Reverse Polarity Input			Forbidden				
Input Filter			Capacitor filter				

Operating Conditions		Min.	Тур.	Max.	Unit
Full load, input voltage range	K78L03-500R3		±2	±4	~~~%
	Others		±2	±3	
Full load, input voltage range	Full load, input voltage range		±0.2	±0.4	70
Nominal input ,10% -100% load		±0.4	±0.6		
20MHz bandwidth, nominal inpu		20	75	mVp-p	
Operating temperature -40 $^\circ\!\!\!\!\!^\circ\!\!\!\!\!\!\!\!\!^\sim\!\!\!\!\sim$			±0.03	%/ ℃	
Nersingling to 05% logalaton abore as			50	250	mV
Normina input, 25% load step ch		0.2	1	ms	
Nominal input			Continuous,	self-recovery	/
	Full load, input voltage range Full load, input voltage range Nominal input ,10% -100% load 20MHz bandwidth, nominal inpu Operating temperature -40°C~ Nominal input, 25% load step ch	Full load, input voltage range K78L03-500R3 Full load, input voltage range Others Full load, input voltage range 20MHz bandwidth, nominal input, 10% -100% load 20MHz bandwidth, nominal input, 10% -100% load Operating temperature -40°C~ +85°C Nominal input, 25% load step change	Full load, input voltage range K78L03-500R3 Full load, input voltage range Others Full load, input voltage range Nominal input, 10% -100% load 20MHz bandwidth, nominal input, 10% -100% load Operating temperature -40°C ~ +85°C Nominal input, 25% load step change	K78L03-500R3 ± 2 Full load, input voltage range $$ ± 2 Full load, input voltage range ± 0.2 Nominal input, 10% -100% load ± 0.4 20MHz bandwidth, nominal input, 10% -100% load 20 Operating temperature -40°C~ $+85°C$ Nominal input, 25% load step change 50 0.2	Full load, input voltage rangeK78L03-500R3- ± 2 ± 4 Full load, input voltage rangeOthers- ± 2 ± 3 Full load, input voltage range- ± 0.2 ± 0.4 Nominal input, 10% -100% load- ± 0.4 ± 0.6 20MHz bandwidth, nominal input, 10% -100% load-2075Operating temperature -40°C~ +85°C ± 0.03 Nominal input, 25% load step change-50250-0.21

Note: *1. Ripple and noise tested with "parallel cable" method, please refer to *DC-DC Converter Application Notes* for specific operation methods; *2. With the load lower than 10%, the maximum ripple and noise of 3.3V/5V output products will be 150mVp-p, 12V/15V output products will be 2%Vo.



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DC/DC Converter

K78Lxx-500R3 Series

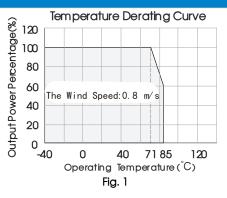
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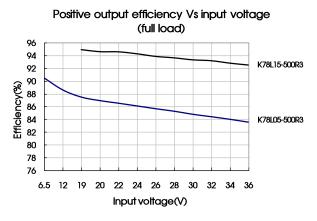
General Specifications							
Item	Operating Conditions	Min.	Typ.	Max.	单位		
Operating Temperature	Derating if the temperature ${\geqslant}71^\circ\!\!\!\!\mathrm{C}$ (see Fig. 1)	-40		85			
Storage Temperature		-55		125	°C		
Pin Welding Resistance Temperature	Welding time: 10s (Max.)			260			
Storage Humidity Non-condensing		5		95	%RH		
Switching Frequency Full load, nominal input		550		850	KHz		
MTBF	MIL-HDBK-217F@25℃	2000			K hours		

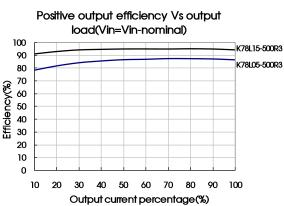
Physical Specifications					
Package Dimensions	10.00*7.20*11.00 mm				
Weight	1.0g (Typ.)				
Cooling Method	Free air convection				

EMC S	Specifications				
EMI	Conducted Disturbance	CISPR22/EN55022	CLASS B (see Fig. 5-2) for recommended circuit)		
LIVII	Radiated Emission	CISPR22/EN55022	2 CLASS B (see Fig. 5-2) for recommended circuit)		
	Electrostatic Discharge	IEC/EN 61000-4-2	Contact ±4KV	perf. Criteria B	
EN 40	Radiation Immunity	IEC/EN 61000-4-3	10V/m	perf. Criteria A	
EMS	EFT	IEC/EN 61000-4-4	$\pm 1 \text{KV}$ (see Fig. 5-1) for recommended circuit)	perf. Criteria B	
	Conducted Disturbance Immunity	IEC/EN 61000-4-6	3Vr.m.s	perf. Criteria A	

Product Characteristic Curve





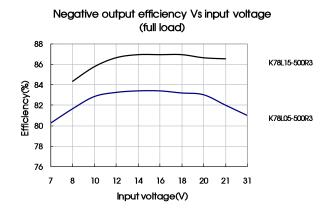


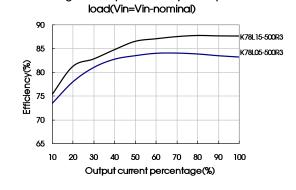
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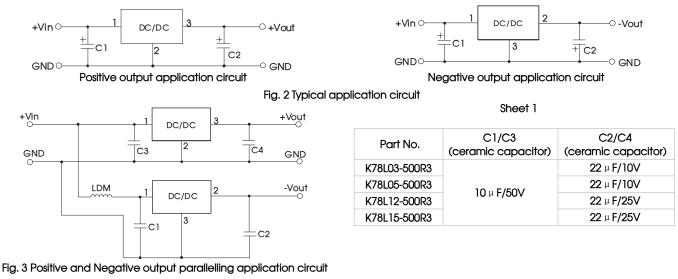




Negative output efficiency Vs output

Design Reference

1. Typical application circuit



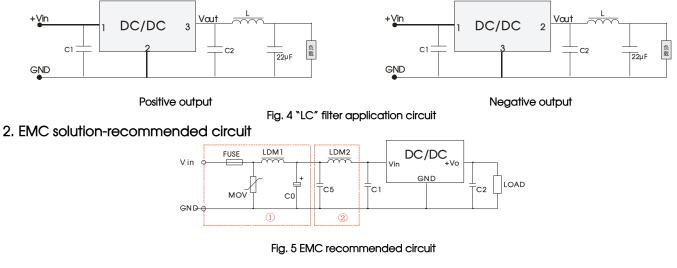
Note:

1. C1 and C2(C3 and C4) are required and should be connected close to the pin terminal of the module.

2. The capacitance of C1 and C2(C3 and C4) refer to Sheet 1, it can be increased properly if required, and tantalum or low ESR electrolytic capacitors may also suffice.

When the products used as the circuit like figure 3, an inductor named as LDM up to 10 µ H is recommended in the circuit to reduce the mutual interference.
Cannot be used in parallel for output and hot swap.

To reduce the output ripple furtherly, it is suggested to connect a "LC" filter at the output terminal, and recommended value of L is 10μ H-47 μ H.





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DC/DC Converter

K78Lxx-500R3 Series



THIRD ANGLE PROJECTION ()

+Vo

3

GND

FUSE	MOV	LDM1	C0	C1/C2	C5	LDM2
Selected based on the actual	S20K30	82µH	680µF /50V	Refer to Sheet 1	4.7µF /50∨	12µH
input current from the customer	320030	οζμπ	000µi /00v		4.7µi /30V	ιζμιι

Note: Part ① in the Fig. 5 is for EMS test, part ② is for EMI filtering; parts ① and ② can be added based on actual requirement.

3. For more information please find the application notes on www.mornsun-power.com

Dimensions and Recommended Layout

General tolerances: ±0.50[±0.020]

10.00 [0.394] 1.80 [0.071] ¢1.20 [¢0.047] Right K78Lxx-500R3 View 11.00 [0.433] Front View ł 2 3 1 0.64 [0.025] 0.64 [0.025] Note : Grid 2.54*2.54mm 4.10 [0.161] 5.40 [0.213] 5.08 [0.200] -Pin-Out Pin **Positive Output Negative Output** Note: Vin Vin Unit :mm[inch] 1 Pin section tolerances :±0.10[±0.004] 2 GND -Vo

Notes:

- 1. Packing information please refer to Product Packing Information. Packing bag number: 58010116;
- 2. The max. capacitive load should be tested within the input voltage range and under full load conditions;
- 3. Without any special statement, all indexes are only specific to positive output application;
- 4. Unless otherwise specified, data in this datasheet should be tested under the conditions of Ta=25°C, humidity<75% when inputting nominal voltage and outputting rated load;
- 5. All index testing methods in this datasheet are based on our Company's corporate standards;
- 6. The performance indexes of the product models listed in this manual are as above, but some indexes of non-standard model products will exceed the above-mentioned requirements, and please directly contact with our technician for specific information;
- 7. Specifications of this product are subject to changes without prior notice.

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