

1W, Fixed input voltage, isolated & unregulated dual output



FEATURES

- Continuous short-circuit protection
- No-load input current as low as 5mA
- Operating temperature range: -40°C to +105°C
- High efficiency up to 85%
- Compact SMD package
- Isolation voltage: 1.5K VDC
- International standard pin-out
- UL62368, EN62368 approval

A05_XT-1WR3 series are specially designed for applications where two isolated voltage is required in a distributed power supply system. They are suitable for; pure digital circuits, low frequency analog circuits, relay-driven circuits and data switching circuits.

Certification	Part No.	Input Voltage (VDC) Output		Efficiency	Max. Capacitive	
		Nominal (Range)	Output Voltage (VDC)	Output Current (mA)(Max./Min.)	(%,Min./Typ.) @ Full Load	Load (µF)*
UL/CE	A0505XT-1WR3	5 (4.5-5.5)	±5	±100/±10	78/82	1200
	A0509XT-1WR3		±9	±56/±6	79/83	470
	A0512XT-1WR3		±12	±42/±5	79/83	220
	A0515XT-1WR3		±15	±34/±4	79/83	220
	A0524XT-1WR3		±24	±21/±3	81/85	100

Note: *The capacitive loads of positive and negative outputs are identical.

Item	Operating Conditions		Min.	Typ.	Max.	Unit
	5VDC output			244/5	257/10	
Input Current (full load / no-load)	5VDC input	9VDC/12VDC output		241/12	254/20	mA
(101110007/110-1000)		15VDC/24VDC output		241/18	254/30	
Reflected Ripple Current*				15		mA
Surge Voltage (1sec. max.)	5VDC input		-0.7		9	VDC
Input Filter			Filter capacitor			
Hot Plug			Unavailable			

Note: * Reflected ripple current testing method please see DC-DC Converter Application Notes for specific operation.

Item	Operating Conditions		Min.	Typ.	Max.	Unit
Output Voltage Accuracy			See tolerance envelope curve(Fig. 1)			
Line Regulation	Input voltage change: ±	±1%			1.2	%/%
	10%-100% load	5VDC output		10	15	%
		9VDC output		8	10	
Load Regulation		12VDC output		7	10	
		15VDC output		6	10	
		24VDC output		5	10	
Dinala & Naias'		Other output		30	75	mVp-p
Ripple & Noise*	20MHz bandwidth	24VDC output		50	100	
Temperature Coefficient	Full load			±0.02		%/ ℃

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DC/DC Converter A05_XT-1WR3 Series

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Continuous, self-recovery

Note: *Ripple and noise are measured by "parallel cable" method, please see DC-DC Converter Application Notes for specific operation;

Item	Operating Conditions	Min.	Typ.	Max.	Unit
Isolation Voltage	Input-output, with the test time of 1 minute and the leak current lower than 1mA				VDC
Isolation Resistance	Input-output, isolation voltage 500VDC 1000				MΩ
Isolation Capacitance	Input-output, 100KHz/0.1V		20		pF
Operating Temperature Derating when operating temperature up to 100°C, (see Fig. 2)		-40		105	
Storage Temperature		-55		125	°C
Casing Temperature Rise	Tα=25℃		15		
Storage Humidity	Non-condensing			95	%RH
Reflow Soldering Temperature*		Peak temp. over 217℃.	≪ 245° C , ma x	imum duratic	n time≤60
Switching Frequency	Full load, nominal input voltage		270		KHz
MTBF	MIL-HDBK-217F@25°C	3500			K hours
Moisture Sensitivity Level (MSL)	IPC/JEDEC J-STD-020D.1	Level 2			

Physical Specifications				
Casing Material	Black flame-retardant and heat-resistant plastic(UL94 V-0)			
Dimensions	15.24*11.40*7.25 mm			
Weight	1.4g (Typ.)			
Cooling Method	Free air convection			

EMC Specifications					
EMI	CE	CISPR32/EN55032 CLASS B (see Fig. 5 for recommended circuit)			
	RE	CISPR32/EN55032 CLASS B (see Fig. 5 for recommended circuit)			
EMS	ESD	IEC/EN61000-4-2 Air ±8kV , Contact ±4kV perf. Criteria B			

Product Characteristic Curve

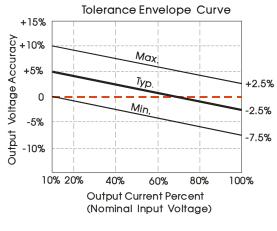


Fig. 1

Temperature Derating Curve

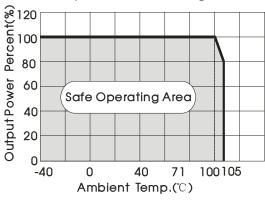
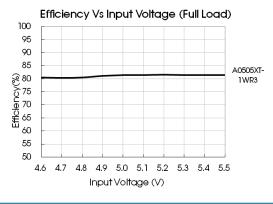


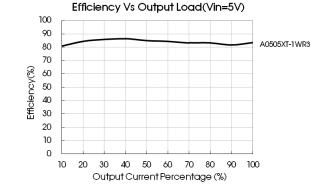
Fig. 2

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Design Reference

1. Typical application circuit

If it is required to further reduce input and output ripple, a filter capacitor may be connected to the input and output terminals, see Fig.3. Moreover, choosing a suitable filter capacitor is very important, start-up problems may be caused if the capacitance is too large. Under the condition of safe and reliable operation, the recommended capacitive load values are shown in Table 1.

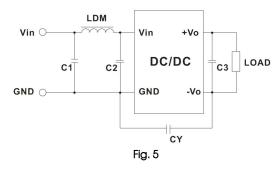
The simplest device for output voltage regulation, over-voltage and over-current protection is a linear voltage regulator with overheat protection that is connected to the input or output end in series (see Fig. 4).



Recommended capacitive load value table (Table 1)

Vin(VDC)	Cin(µF)	Vo (VDC)	Cout(µF)
		±5	4.7
F	4.7	±9	2.2
5	4./	±12	1
		±15/±24	1

2. EMC solution-recommended circuit



EMC recommended circuit value table (Table 2)					
Input voltage 5VDC	Output voltage(VDC)		5/9	12/15/24	
	EMI	C1/C2	4.7µF /25∨	4.7µF /25∨	
		СҮ		1nF/2KVDC HEC C1206X102K202T JOHANSON 202R18W102KV4E	
		C3	Refer to the Cout in table 1		
		LDM	6.8µH	6.8µH	

Note: In the case of actual use, the requirements for EMI are high, it is subject to CY.

3. For more information please find DC-DC converter application notes on www.mornsun-power.com



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[0.480]

12.20

GND

Vin

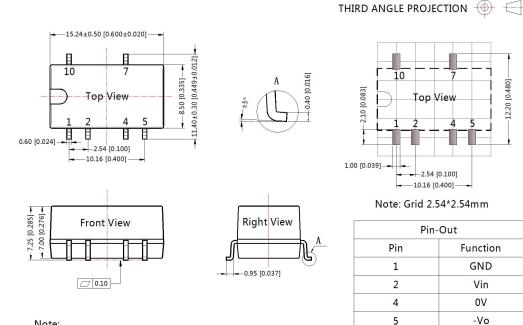
0V

-Vo

+Vo

NC

Dimensions and Recommended Layout



Note: Unit: mm[inch] Pin section tolerances: ±0.10[±0.004] General tolerances: ±0.25[±0.010]

NC: Pin to be isolated from circuitry

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Notes:

- Packing information please refer to Product Packing Information which can be downloaded from www.mornsun-power.com. Tube 1. Packing bag number: 58210023, Roll Packing bag number: 58210034;
- If the product is not operated within the required load range, the product performance cannot be guaranteed to comply with all 2. parameters in the datasheet;
- The maximum capacitive load offered were tested at input voltage range and full load; 3.
- 4. Unless otherwise specified, parameters in this datasheet were measured under the conditions of Ta=25°C, humidity<75%RH with nominal input voltage and rated output load;
- All index testing methods in this datasheet are based on our Company's corporate standards; 5.
- We can provide product customization service, please contact our technicians directly for specific information; 6.
- Products are related to laws and regulations: see "Features" and "EMC"; 7.
- Our products shall be classified according to ISO14001 and related environmental laws and regulations, and shall be handled by 8. qualified units.

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